

Quarterly News Bulletin

September 2009



ASD1000 qualification following schedule
The qualification of the ASD1000, Octal 12-bit 20-80 MSPS ADC with LVDS, is on schedule. Full volume manufacturing release is targeted to 1st October 2009

ASD1000 market feedback
The ASD1000 has been very well received in the market, and the feedback from customers on performance and power dissipation is confirming that this part is the best power-performance product in its class. The first design wins have already been confirmed, and several customers in multiple applications are testing the part in their systems.

ASD0501 providing enabling technology for TETRA2
The ASD0501, a 12-bit 20-80 MSPS ADC, has been evaluated for use in Professional Mobile Radio (PMR) systems. Results from the evaluation are showing that the extreme low power dissipation of the ASD0501 will act as enabling technology for the upcoming implementation of the new TETRA2 standard for PMR systems. ASD is collaborating with leading consultancy companies on TETRA2 solutions.

New products for communication and test systems introduced
ASD is expanding its GLACIER family with 3 new products;
The ASD0610 is a quad 14-bit 20-80 MSPS ADC which offers a highly versatile functionality. The part will dissipate very low power, 110 mW/ch at 80MSPS, and at the same time provide very good IF performance, 74 dB SFDR at Fin of 140 MHz. The main target markets are within communication applications such as

Diversity Receivers and Software Defined Radios. Hi End Ultrasound will also benefit from the very lower power dissipation of the ASD0610.

The ASD5010 is a multimode 8-bit 160-640 MSPS ADC targeting digital oscilloscopes. The converter is implementing a patent pending architecture making it possible to trade off accuracy and power dissipation. Interleaving enables user selectable combinations of number of active channels and sampling speed. Ultra low power dissipation is implemented, 470 mW at 640 MSPS.

The ASD5020 is a multimode 12-/ 14-bit ADC targeting precision oscilloscopes. Also in this part the patent pending architecture described above is implemented, allowing for different combinations of 12- or 14-bit resolution in combination with sampling speeds up to 640 MSPS. Both ASD5010 and ASD5020 also have implemented an analog cross-point switch that significantly reduces board BOM cost.

Sampling of all three products is estimated to mid February 2010, with a production release in June 2010.

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